REMARKS

This paper responds to the Advisory Action dated on June 21, 2005.

Claims 1, 3, 5, 6, 8, 11, 13, 15, and 20 are amended, no claims are canceled, and no claims are added; as a result, claims 1-27 remain pending in this application.

Applicant incorporates all prior responses to preserve all issues for appeal.

Allowable Subject Matter

Claims 3 and 6 were objected to as being dependent upon a rejected base claim, but were indicated to be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims. Applicant has rewritten claim 3 and 6 in independent form. These claims are not narrowed. These claims are not amended in view of any rejection of these claims under the Patent Act. Applicant requests that claims 3 and 6 be passed to allowance.

Applicant further requests that dependent claims 25-27 be entered and allowed as these claims depend from claims 3 and 6 and contain further limitations over the base claims.

§103 Rejection of the Claims

Claims 1, 2, 4, 5, and 7-24 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Shibata et al. (U. S. Patent No. 5,754,838) in view of IBM (16 Mb Double Data Rate Synchronous Graphics RAM). Applicant respectfully traverses the rejection, and submits that the claims amendments contained herein place the claims in condition for allowance.

Claim 1 recites, in part, "a logic circuitry coupled to the memory array configurable to operate the single memory device in a first mode having delayed lock loop (DLL) capability and in a second mode having non-DLL capability, wherein the logic circuitry comprises an Extended Mode Register adapted to enter the first mode having the DLL capability upon receiving a Load Mode Register command." Applicant can not find this feature in the applied references. Accordingly, applicant submits that claim 1 and its dependent claims are allowable. Reconsideration and allowance of claim 1 and its dependent claims are requested.

Claim 5 recites, in part, "wherein the logic circuitry comprises an Extended Mode Register adapted to enter the first mode having the DLL capability upon receiving a Load Mode

Register command on the Extended Mode Register." Applicant can not find this feature in the applied references. Accordingly, applicant submits that claim 5 and its dependent claims are allowable. Reconsideration and allowance of claim 5 and its dependent claims are requested.

Claim 8 recites, in part, "wherein the logic circuitry comprises an Extended Mode Register adapted to enter the first mode having the DLL capability upon receiving a Load Mode Register command." Applicant can not find this feature in the applied references. Accordingly, applicant submits that claim 8 and its dependent claims are allowable. Reconsideration and allowance of claim 8 and its dependent claims are requested.

Claim 11 recites, in part, "wherein the logic circuitry comprises an Extended Mode Register adapted to enter the first mode having the DLL capability upon receiving a Load Mode Register command." Applicant can not find this feature in the applied references. Accordingly, applicant submits that claim 11 and its dependent claims are allowable. Reconsideration and allowance of claim 11 and its dependent claims are requested.

Claim 13 recites, in part, "wherein the logic circuitry comprises an Extended Mode Register adapted to enter the first mode having the DLL capability upon receiving a command." Applicant can not find this feature in the applied references. Accordingly, applicant submits that claim 13 and its dependent claims are allowable. Reconsideration and allowance of claim 13 and its dependent claims are requested.

Claim 15 recites, in part, "wherein the logic circuitry comprises an Extended Mode Register adapted to enter the first mode having the DLL capability upon receiving a command." Applicant can not find this feature in the applied references. Accordingly, applicant submits that claim 15 and its dependent claims are allowable. Reconsideration and allowance of claim 15 and its dependent claims are requested.

Claim 20 recites, in part, "wherein the logic circuitry comprises an Extended Mode Register adapted to enter the first mode having the DLL capability upon receiving a command." Applicant can not find this feature in the applied references. Accordingly, applicant submits that claim 20 and its dependent claims are allowable. Reconsideration and allowance of claim 20 and its dependent claims are requested.

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.116 – EXPEDITED PROCEDURE   Page 10
Serial Number: 10/629,957   Dkt: 303.519US2
Filing Date: July 30, 2003
Title: DUAL MODE DDR SDRAM/SGRAM

CONCLUSION

Applicant respectfully submits that the claims are in condition for allowance and notification to that effect is earnestly requested. The Examiner is invited to telephone Applicant's attorney (612) 349-9587 to facilitate prosecution of this application.

If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

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Date _5 July '05_

By _____
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop AF, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this _5_ day of July, 2005.

Tina Kohul

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Name

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Signature